

Figure 1

## Logical Layout and Connections

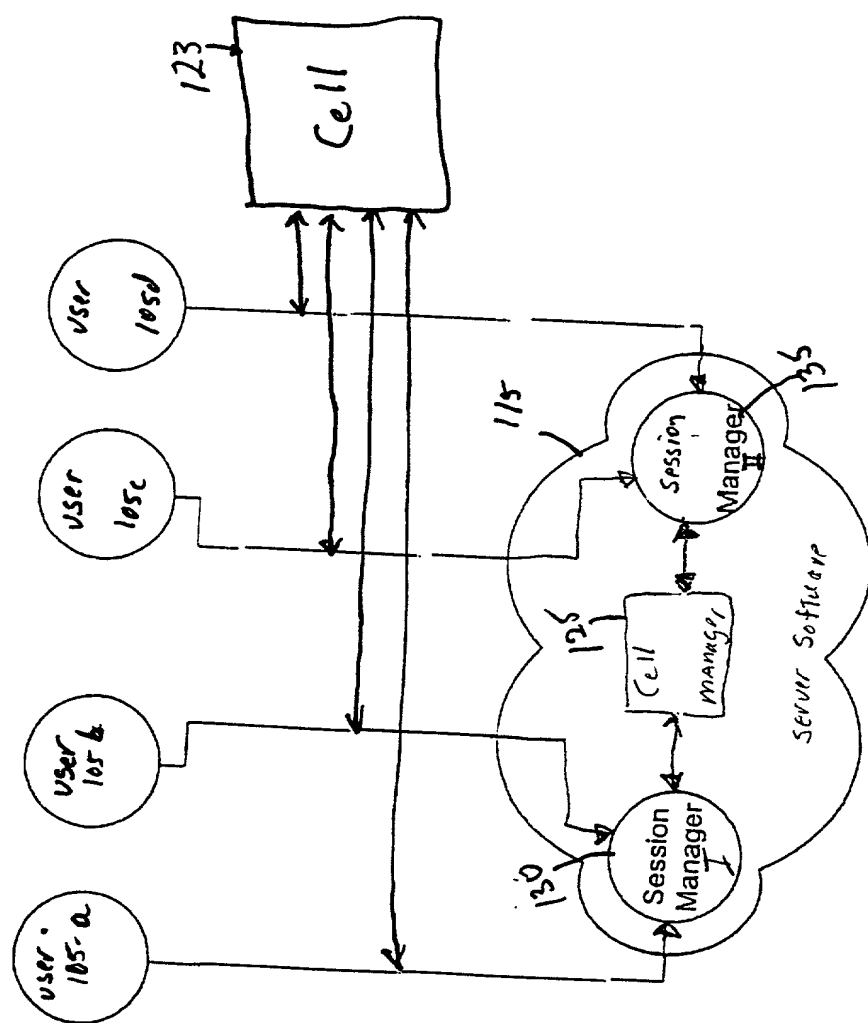


Figure 2

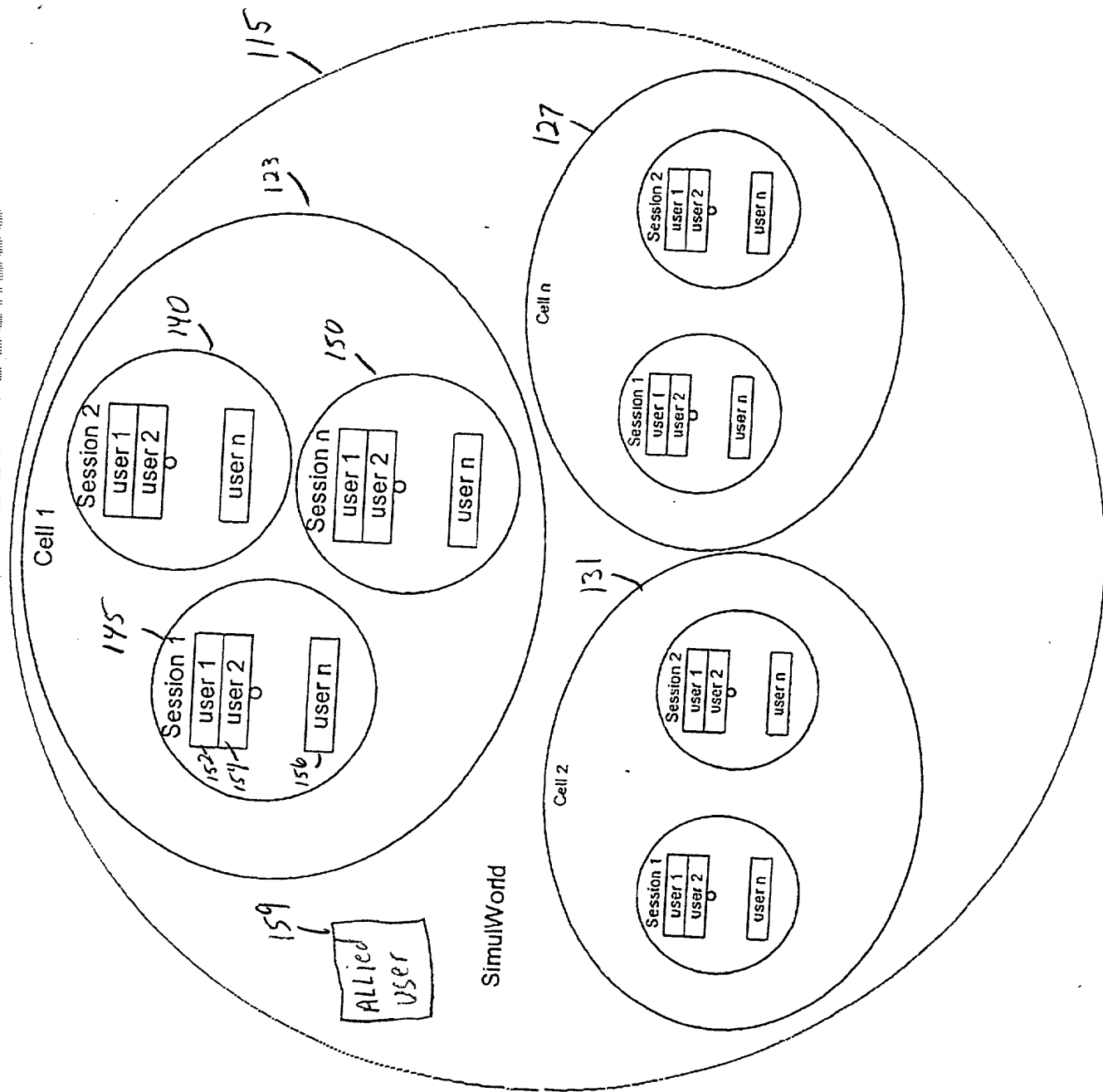


Figure 3

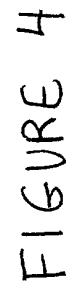


FIGURE 4